

Remarks

The enclosed is responsive to the Examiner's Office Action mailed 3/15/05 in which all claims were rejected under 35 USC 102(e) in light of the Krishna reference (US Patent Pub. No. US2003/0014627 A1. The Applicant has canceled all claims and has new claims 52 through 55 for examination.

The Applicant highlights to the Examiner the following features of the Applicant's independent claim 52 (emphasis added):

iii) a first region of memory coupled to said inbound IPsec decryption logic circuitry, said first region of memory to store security policy information that indicates whether or not said inbound IPsec packet warrants said IPsec decryption processing;

viii) a third path from said first outbound buffer to said second outbound buffer that flows through said IPsec encryption logic circuitry, a fourth path from said first outbound buffer to said second outbound buffer that does not flow through said IPsec encryption logic circuitry, said outbound IPsec packet to flow along said third path if said outbound security policy information indicates that said outbound IPsec packet warrants said IPsec encryption processing, said outbound IPsec packet to flow along said second path if said outbound security policy information indicates that said outbound IPsec packet does not warrant said IPsec encryption processing.

Referring to Figure 4 of the Applicant's specification, first path from buffer 403 to buffer 416 that flows through the IPsec Decryption accelerator 402 can be viewed as an embodiment of the first path recited in the Applicant's claims, and, path 417 can be viewed as an embodiment of the second path recited above in the Applicant's claims. See Also, paragraphs [0039] and [0040] of the Applicant's specification.

Essentially, the Applicant's claimed subject matter includes different paths for the inbound IPSec packet whose usage depends on whether "security policy information" indicates that IPSec encryption processing is appropriate for the inbound IPSec packet (the first path through the IPSec decryption logic circuitry being used if so, the second path that avoids the IPSec decryption logic circuitry being used if not).

The discussion of Krishna from [0051] to [0056] of Krishna does not discuss this ability.

The Applicant therefore respectfully submits that the presently claimed subject matter is patentable over the Krishna reference.

Conclusion

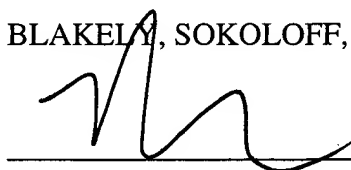
If there are any additional charges, please charge Deposit Account No. 02-2666. If a telephone interview would in any way expedite the prosecution of this application, the Examiner is invited to contact Robert B. O'Rourke at (408) 720-8300.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: _____

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Robert B. O'Rourke
Reg. No. 46,972

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025-1030
(408) 720-8300